

FIG. 1A

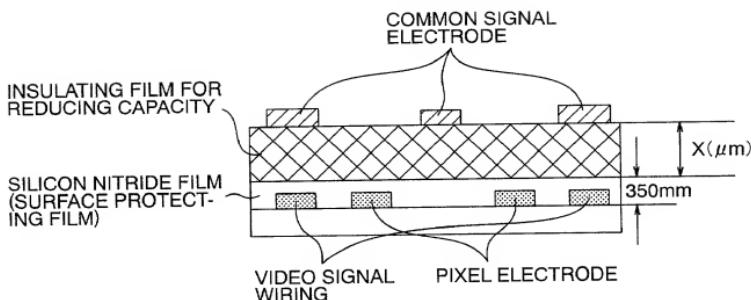


FIG. 1B

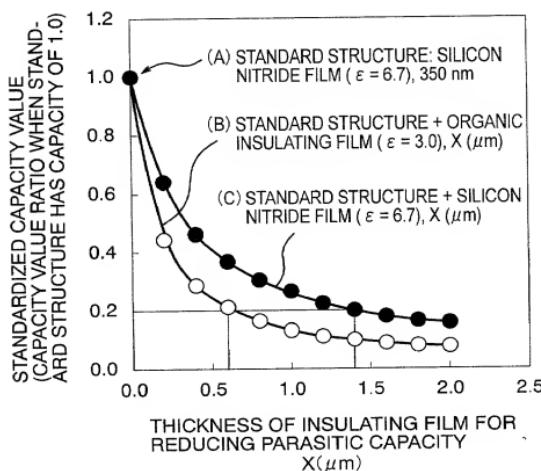


FIG. 2A

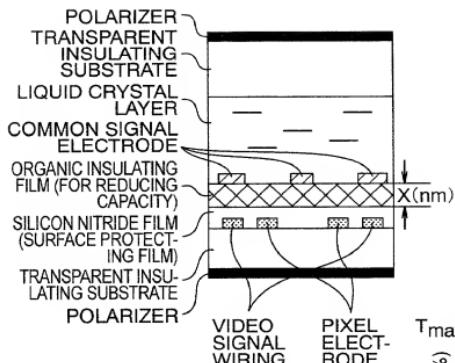


FIG. 2C

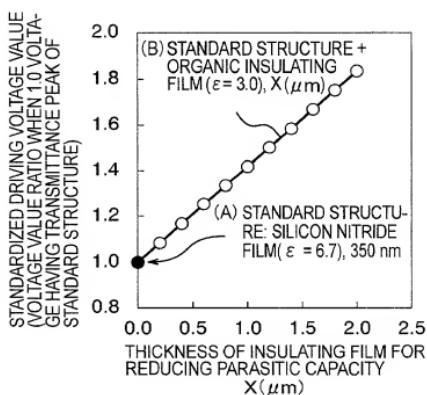


FIG. 2B

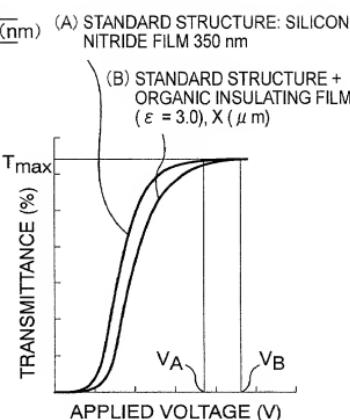


FIG. 3

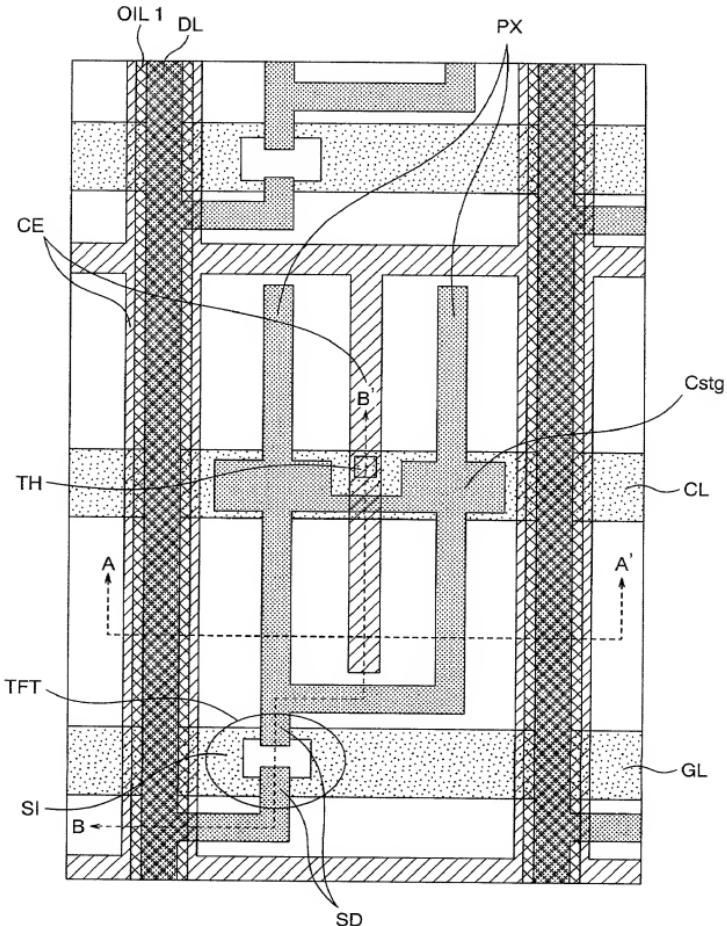


FIG. 4

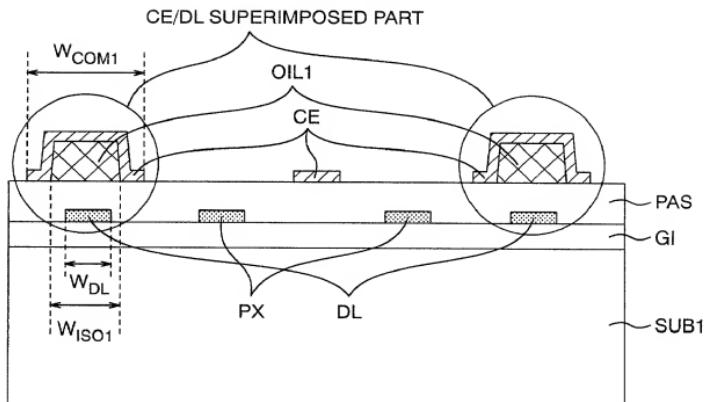


FIG. 5

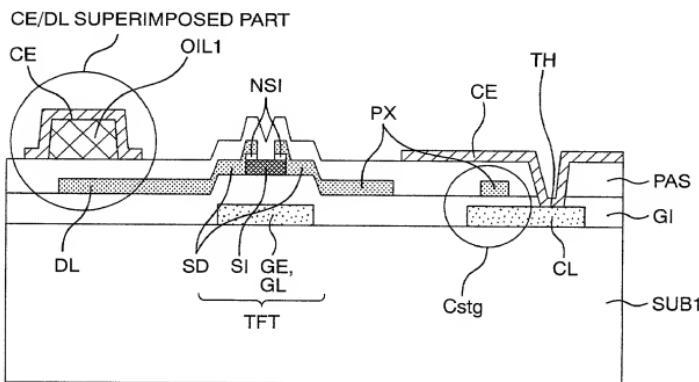


FIG. 6

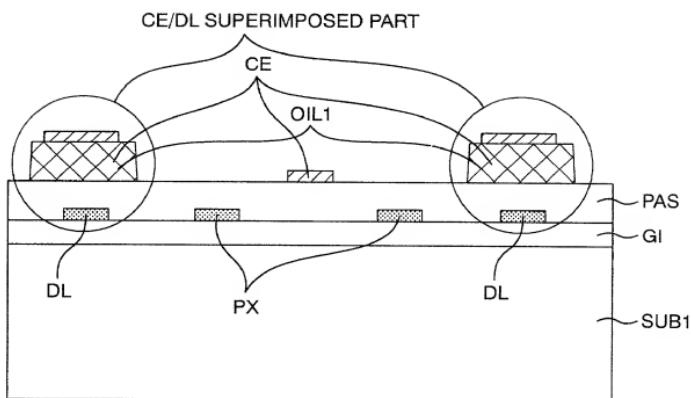


FIG. 7

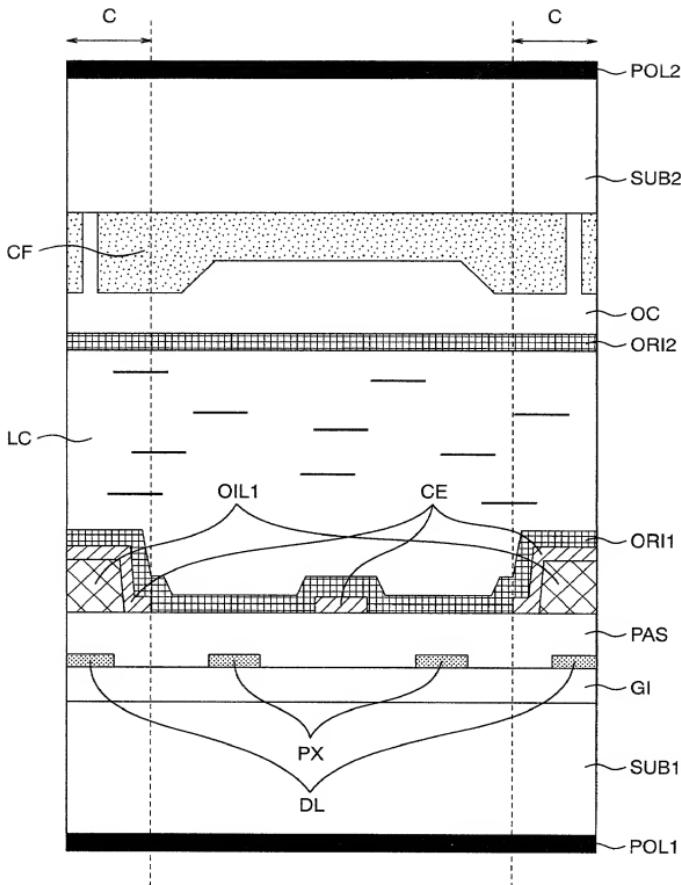


FIG. 8

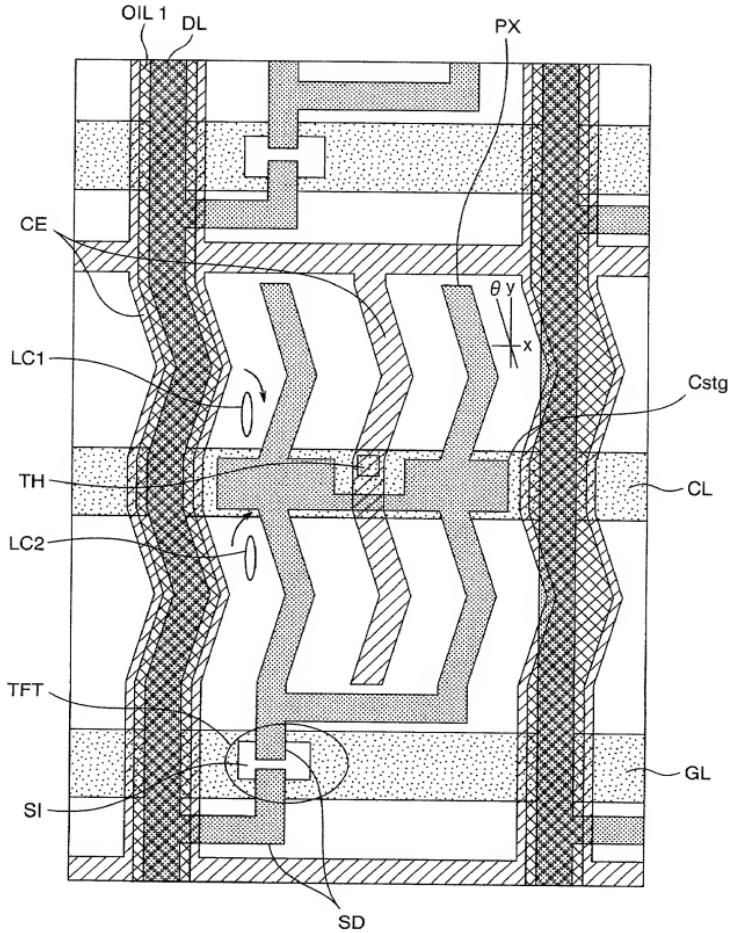


FIG. 9

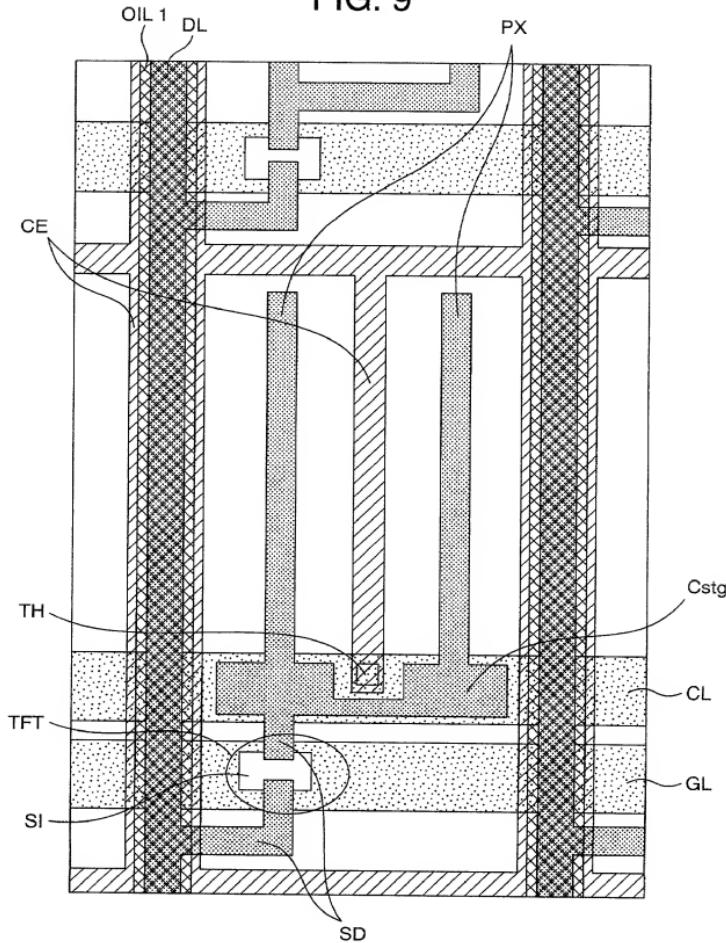


FIG. 10

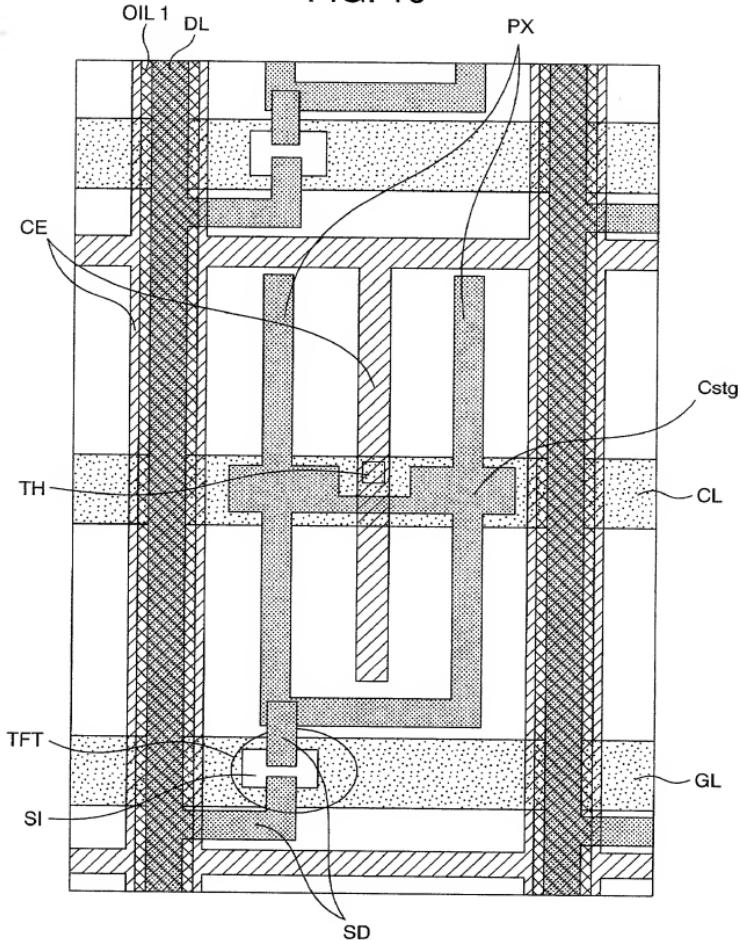


FIG. 11

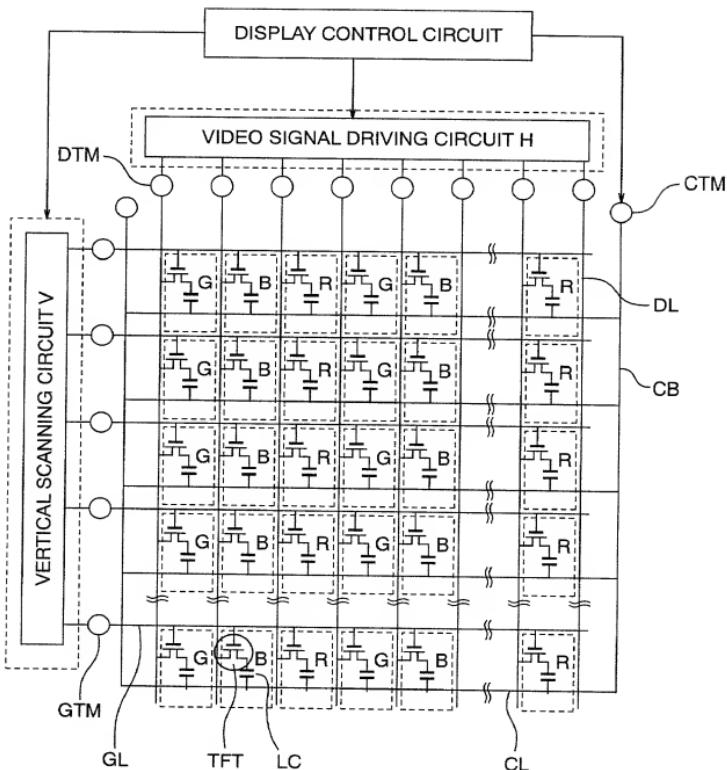


FIG. 12A

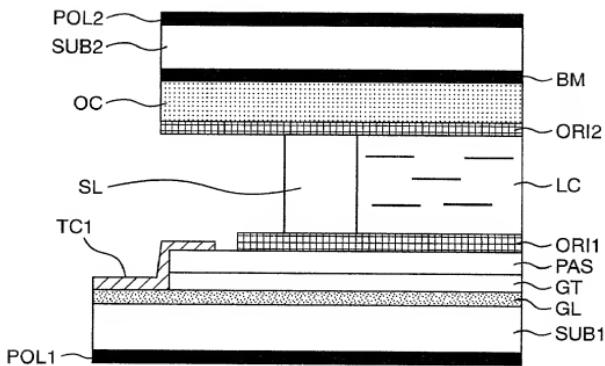


FIG. 12B

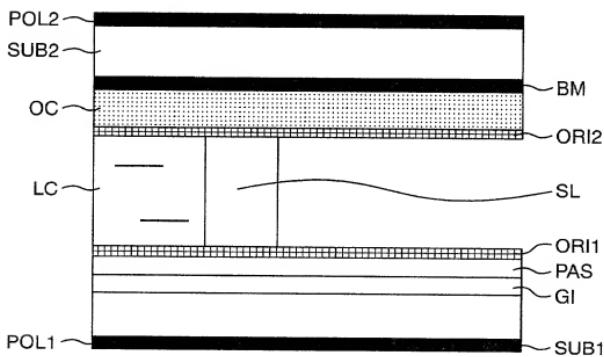


FIG. 13A

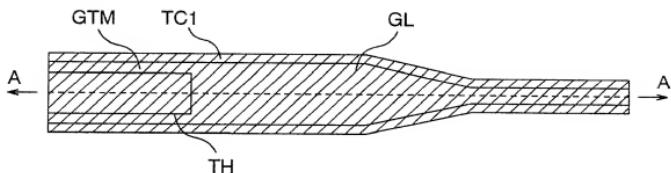


FIG. 13B

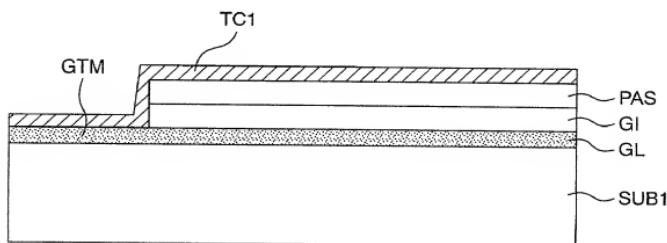


FIG. 14A

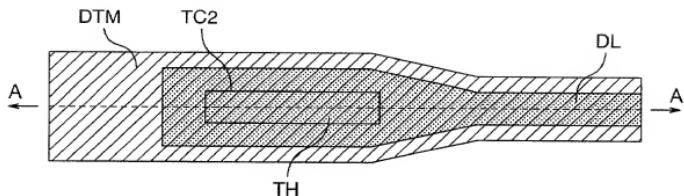


FIG. 14B

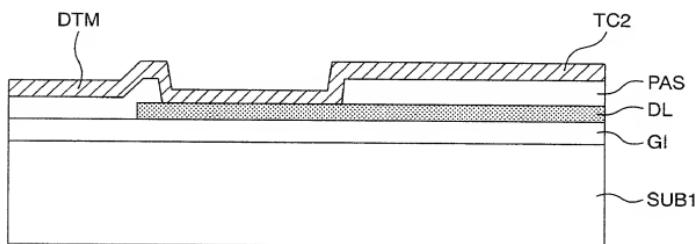
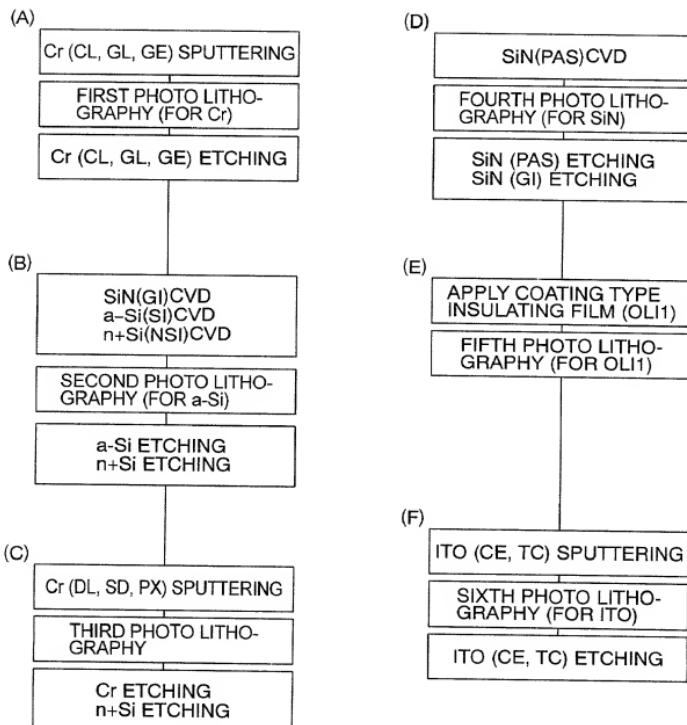


FIG. 15

TOP SECRET//DTIC 60



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FIG. 16A



FIG. 16A'

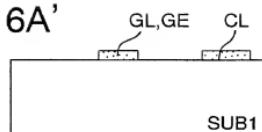


FIG. 16B

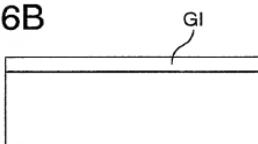


FIG. 16B'

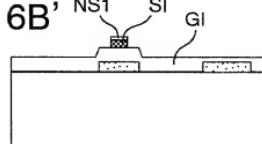


FIG. 16C

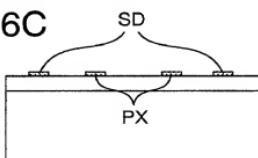


FIG. 16C'

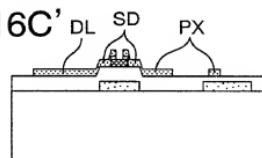


FIG. 16D

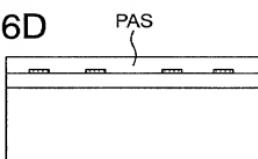


FIG. 16D'

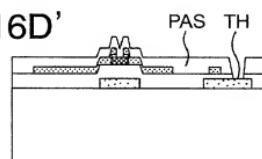


FIG. 16E

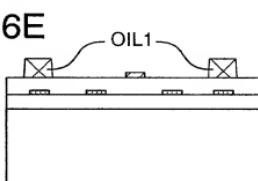


FIG. 16E'

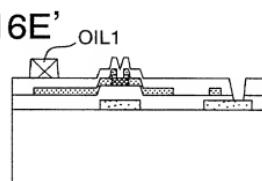


FIG. 16F

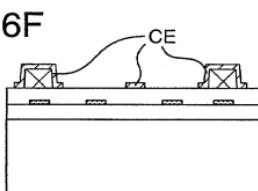


FIG. 16F'

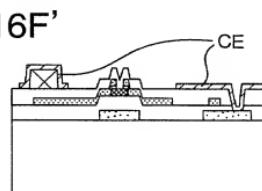


FIG. 17

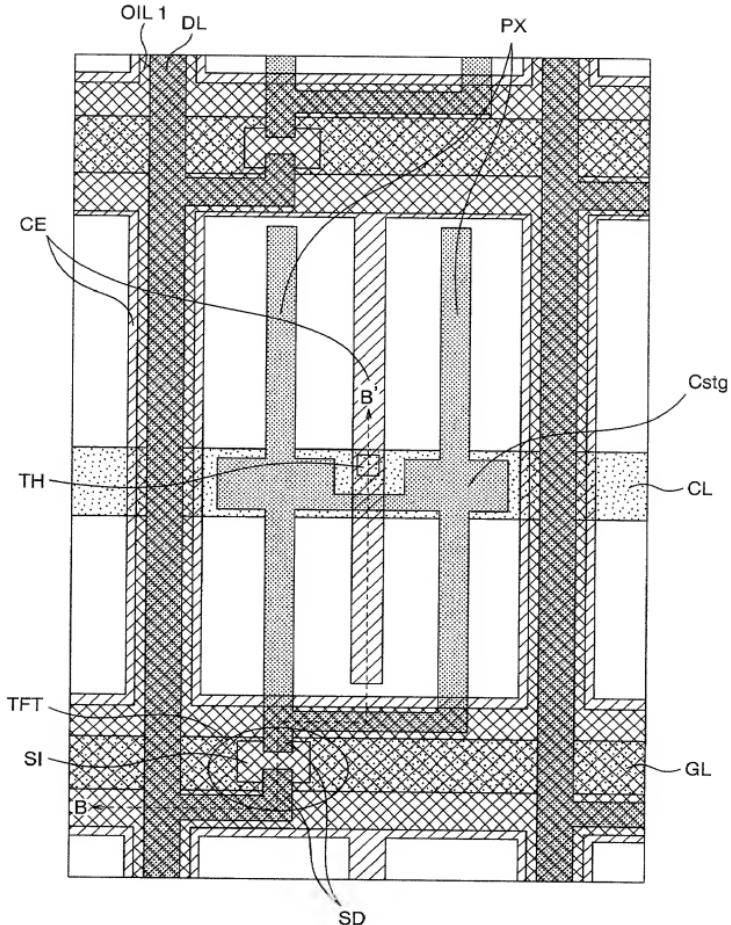


FIG. 18

TOKYO ELECTRON LTD.

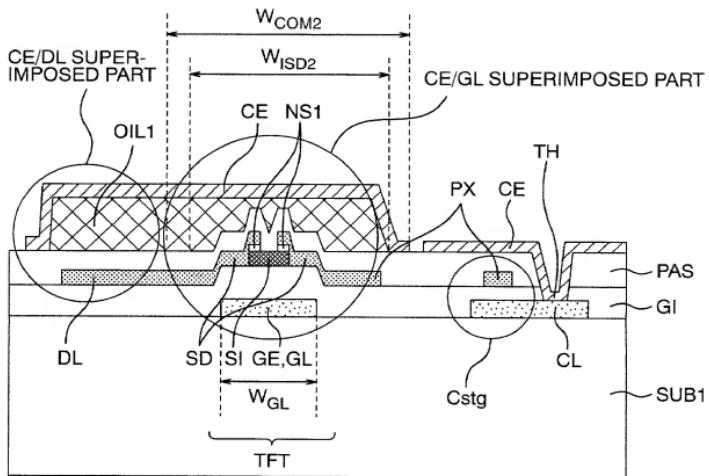


FIG. 19

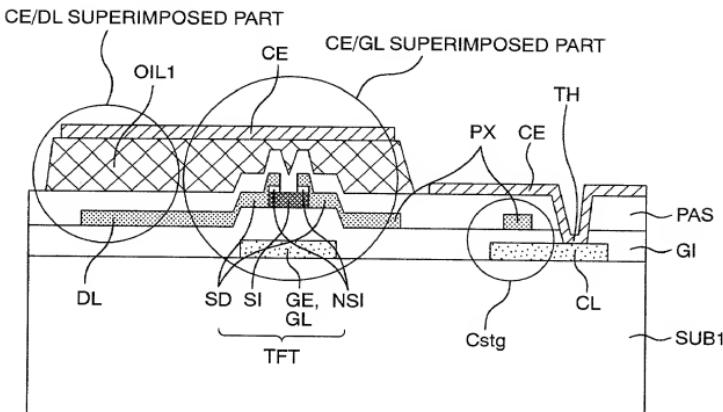
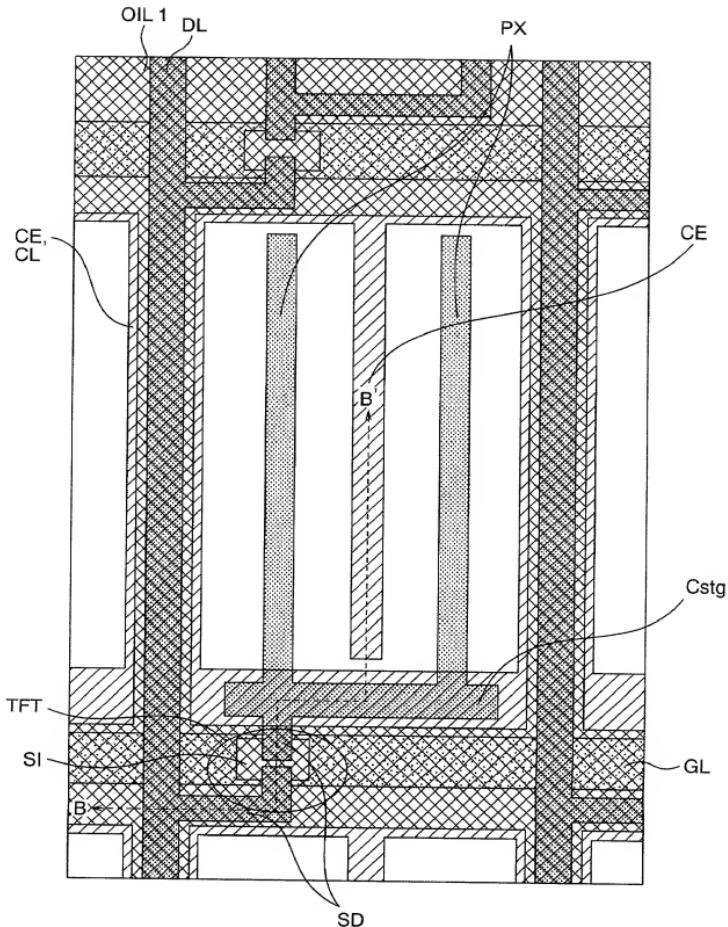


FIG. 20



101380-14134660

FIG. 21

TOTEGO: TETRAGEO

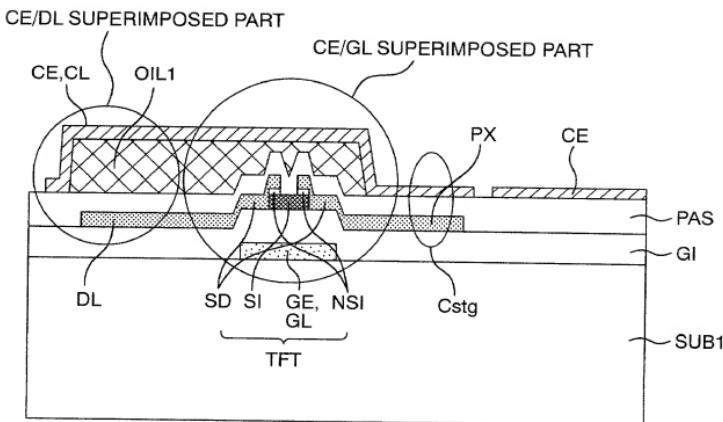


FIG. 22

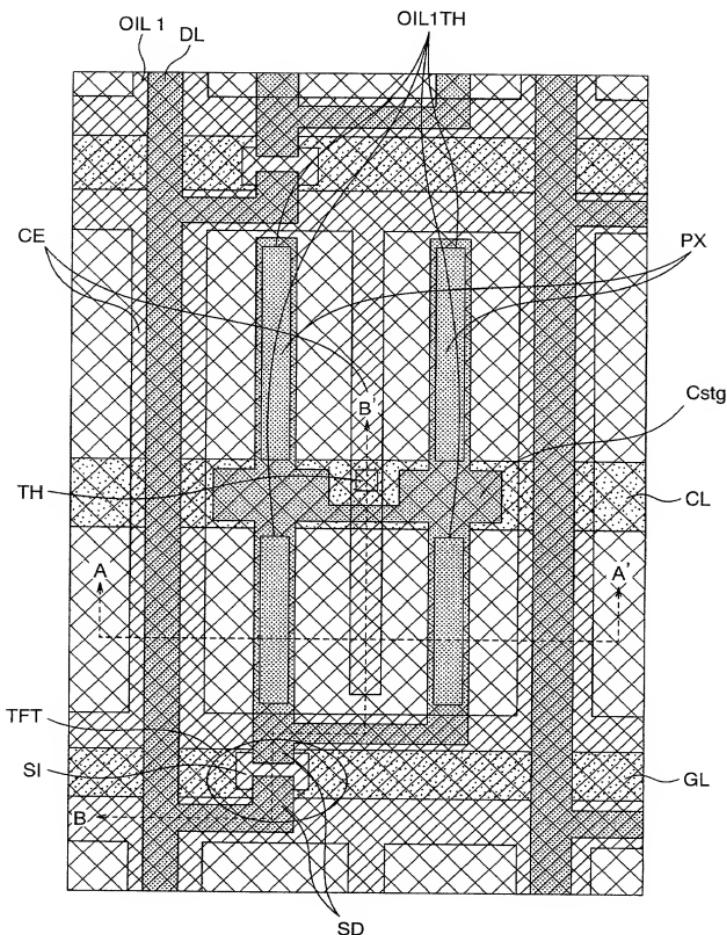


FIG. 23

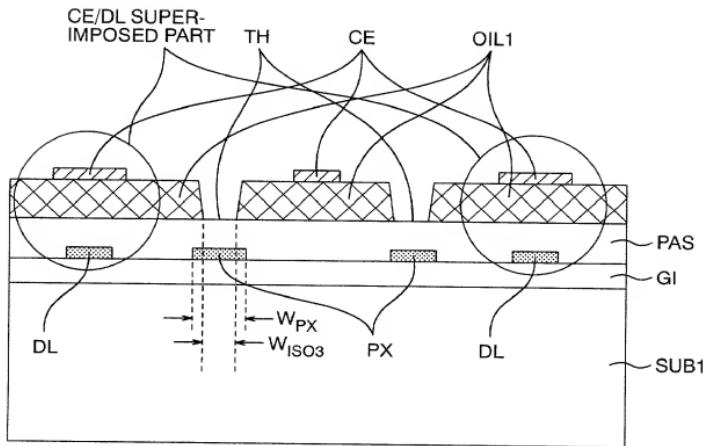


FIG. 24

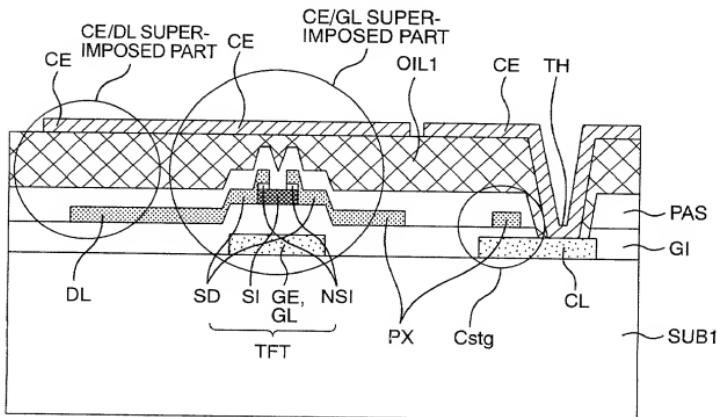


FIG. 25

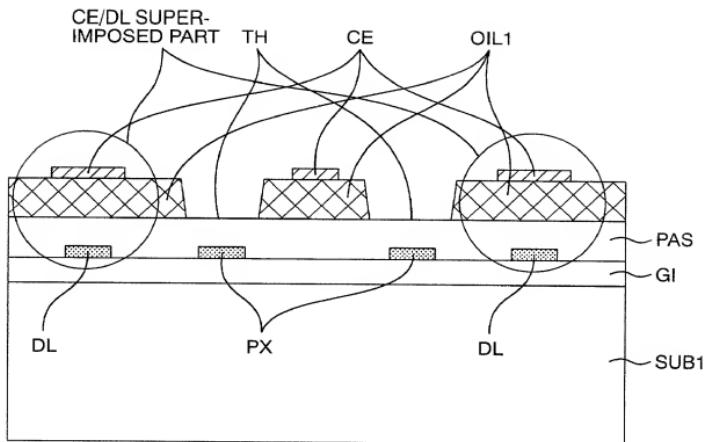


FIG. 26

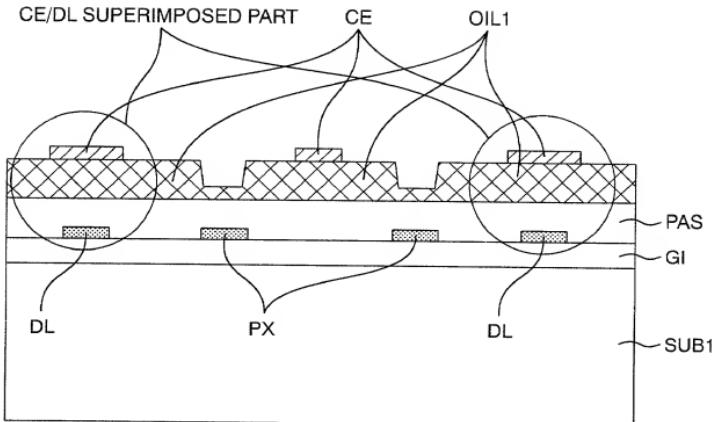


FIG. 27

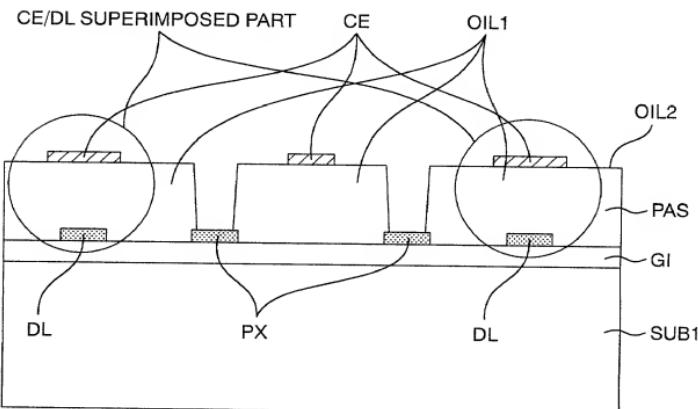


FIG. 28

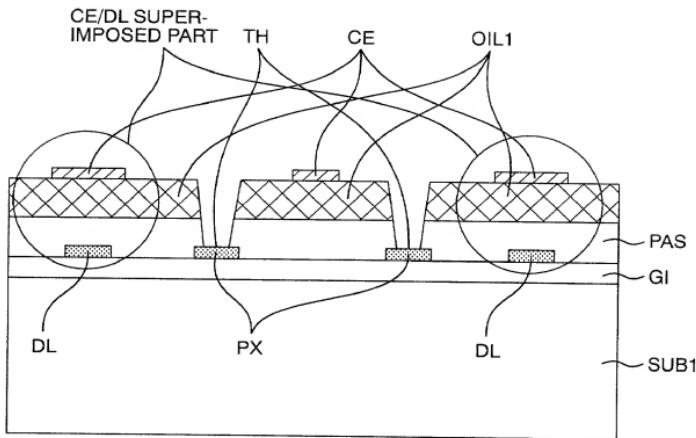


FIG. 29

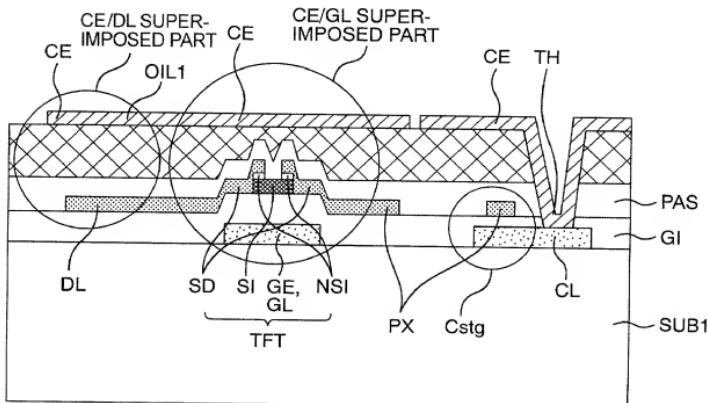
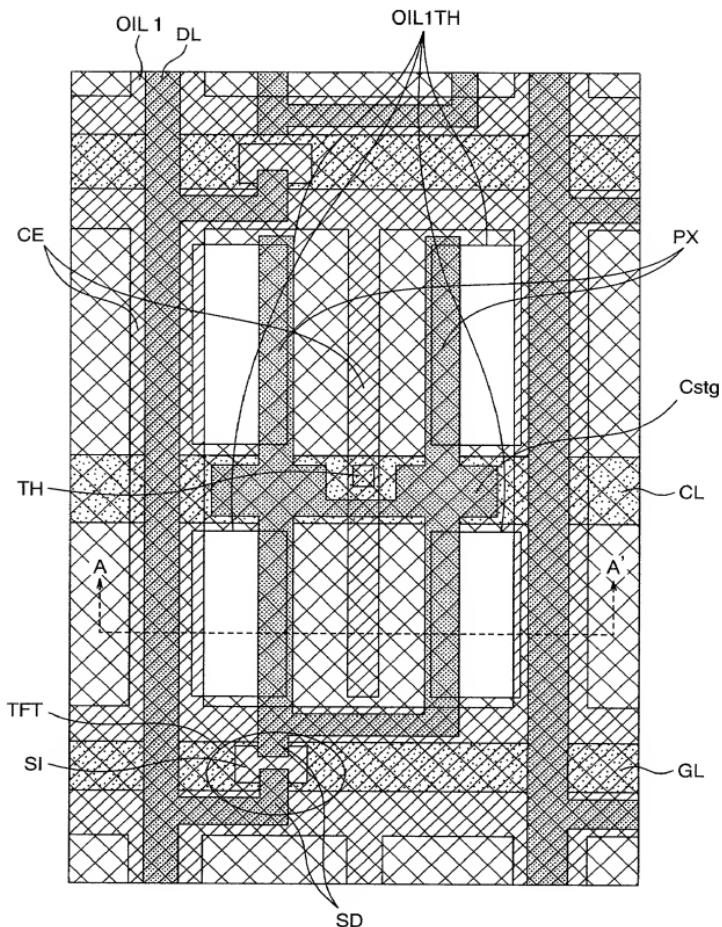


FIG. 30



TOE80-T443450

FIG. 31

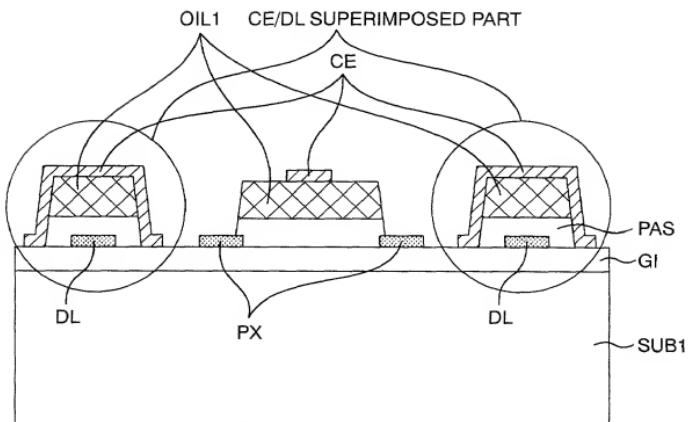


FIG. 32A

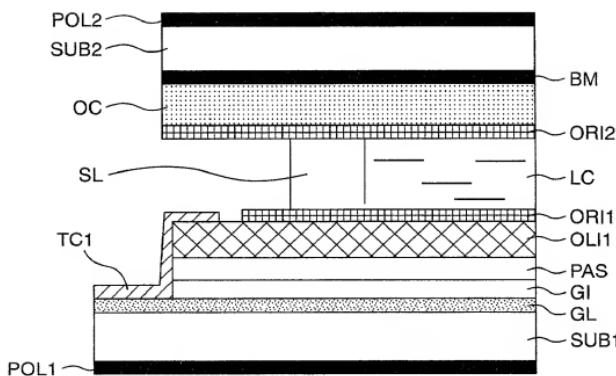


FIG. 32B

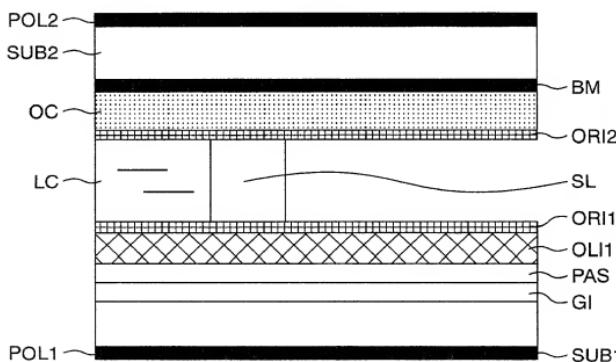


FIG. 33A

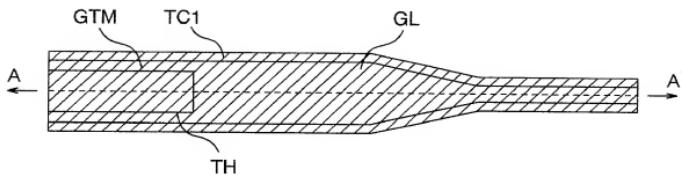


FIG. 33B

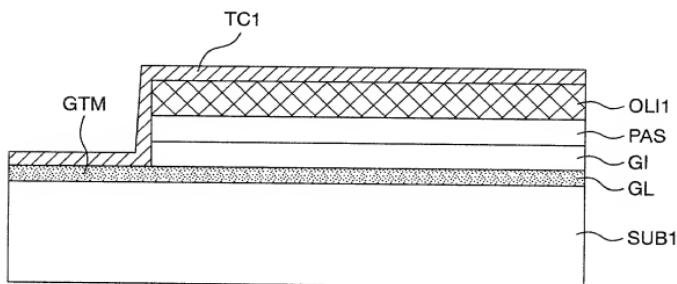


FIG. 34A

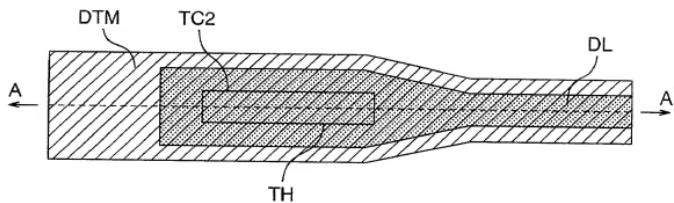


FIG. 34B

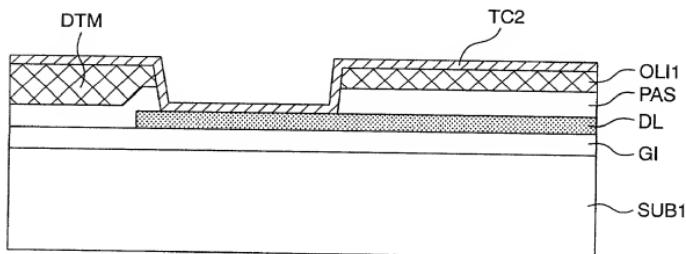


FIG. 35

TOTAL PROCESS

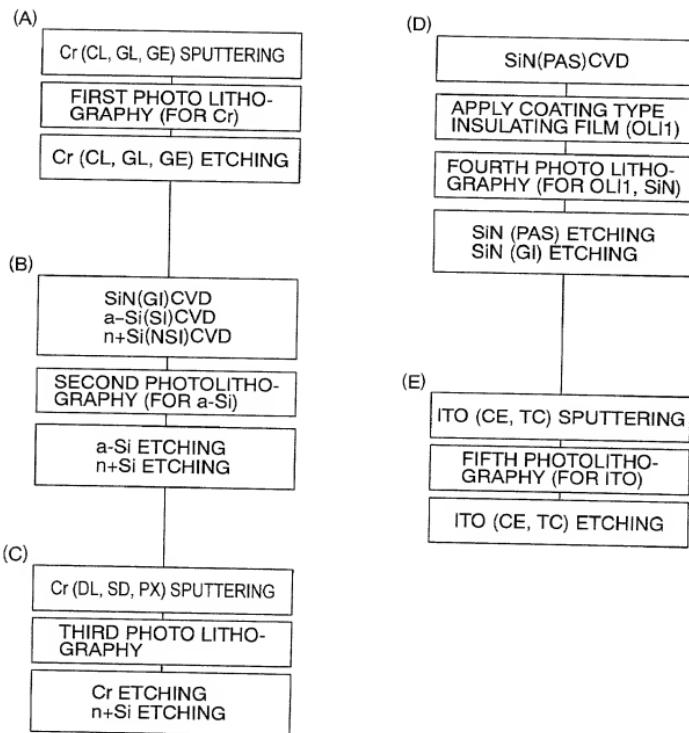


FIG. 36A

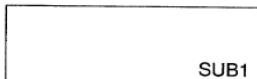


FIG. 36A'

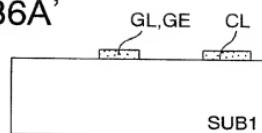


FIG. 36B

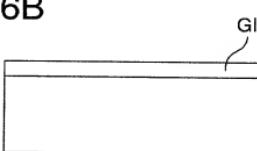


FIG. 36B'

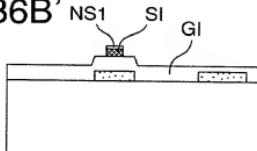


FIG. 36C

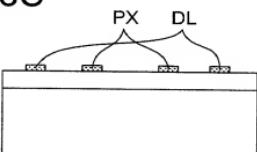


FIG. 36C'

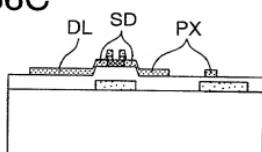


FIG. 36D

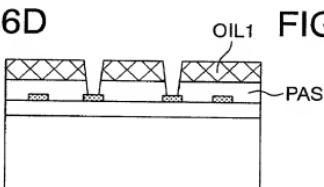


FIG. 36D'

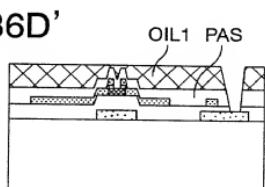


FIG. 36E

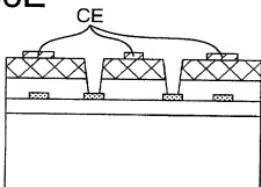


FIG. 36E'

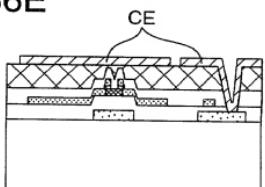


FIG. 37

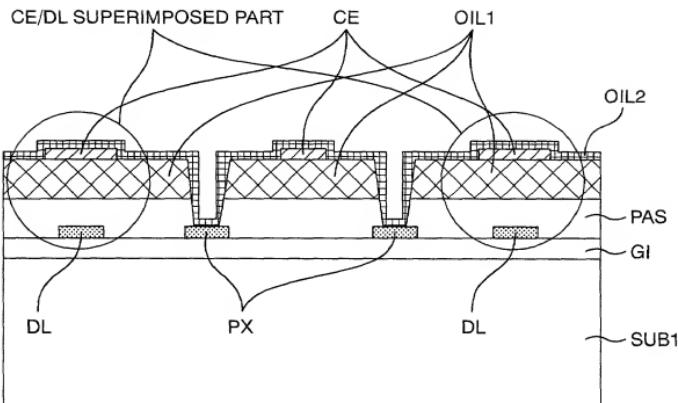


FIG. 38

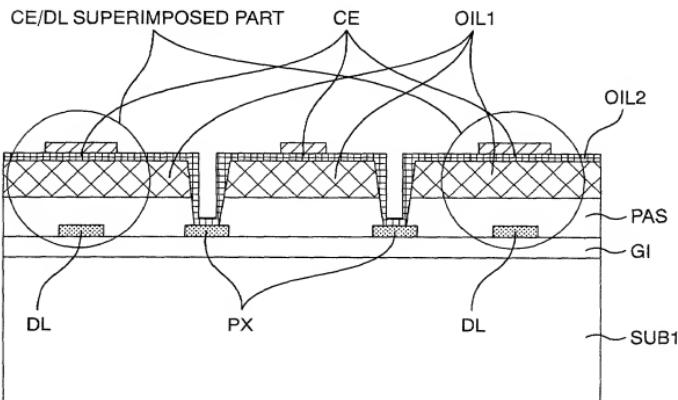


FIG. 39

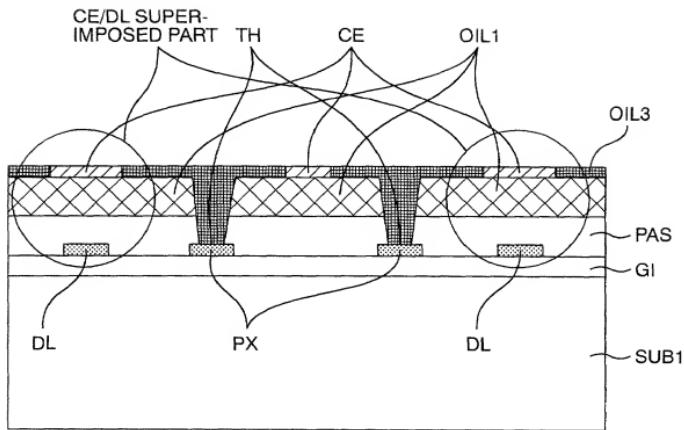


FIG. 40

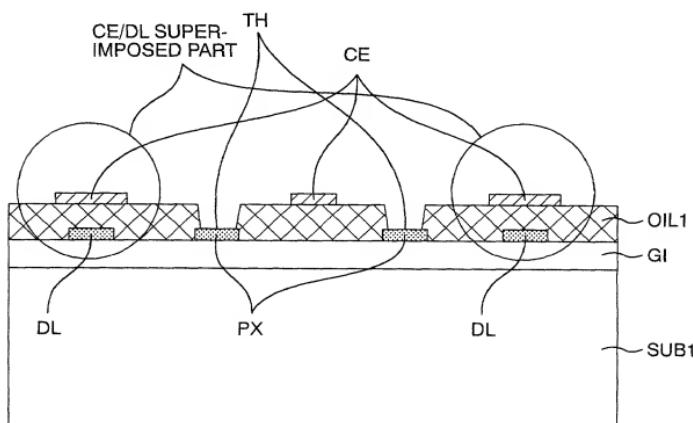


FIG. 41

CE/DL SUPERIMPOSED PART

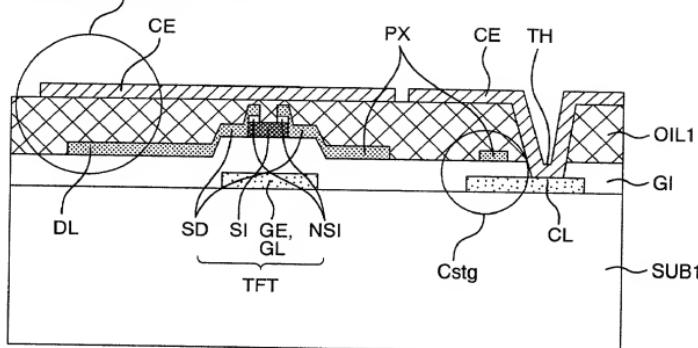


FIG. 42

CE/DL SUPER-
IMPOSED PART

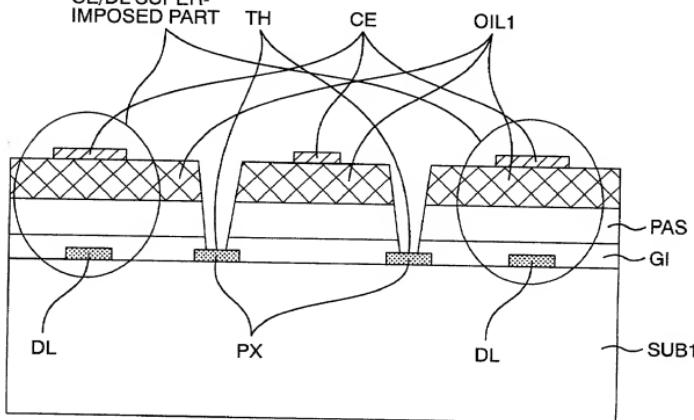


FIG. 43

